



INTERNATIONAL CONFERENCE ON
**“VLSI, SIGNAL PROCESSING, POWER SYSTEMS,
 ILLUMINATION & LIGHTING CONTROL,
 COMMUNICATION AND EMBEDDED SYSTEMS”**

23-24 May 2019



SCHEDULE

23- 05- 2019			
8.30 – 9.15 AM	Registration		
9.15 – 11.00 AM	Inaugural Function	Dr. B. N. Suresh Chancellor, Indian Institute of Space Science & Technology Thiruvananthapuram	Sambhram
11.00 – 11.30 AM	TEA BREAK		
11.30 – 1.00 PM	Key Note Address on “Advanced Electronics and Aerospace and Future Trends”	Dr. B. N. Suresh Chancellor, Indian Institute of Space Science & Technology Thiruvananthapuram	Sowparnika
1.00 – 2.00 PM	LUNCH BREAK		
2.00- 3.15 PM	Session 1 a: Signal Processing	Papers: 48, 50, 100, 121, 134, 135	Sowparnika
	Session 1 b: Signal Processing	Papers: 38, 54, 58, 71, 82	DEC Classroom
3.15 – 3.30 PM	TEA BREAK		
3.30 – 4.45 PM	Session 2 a: Communication	Papers: 7, 14, 19, 46, 117	Sowparnika
	Session 2 b: Power & Control Engineering	Papers: 56, 66, 72, 83, 84, 86	DEC Classroom
5.15 – 7.00 PM	LOCAL TRIP		
7.00 PM Onwards	BANQUET DINNER		
24- 05- 2019			
9.30 – 11.00 AM	Key Note Address on “Building Smart Systems for the Future: Challenges and Opportunities”	Dr. C. P. Ravikumar Texas Instruments, India	Sowparnika
11.00 – 11.15 AM	TEA BREAK		
11.15 – 1.00 PM	Session 3 a: VLSI & Embedded Systems	Papers: 3, 41, 51, 62, 65, 69	Sowparnika
	Session 3 b: VLSI & Embedded Systems	Papers: 27, 34, 101, 106, 120, 136	DEC Classroom
1.00 – 2.00 PM	LUNCH BREAK		
2.00- 2.45 PM	Key Note Address on “Convergence of Fundamental and Applied Research in Advanced Electronics Engineering Field: Necessity and Challenges”	Dr. Shubhakar Kalya Singapore University of Technology & Design, Singapore	Sowparnika
2.45 – 3.45 PM	Session 4 a: Communication	Papers: 36, 43, 110, 114	Sowparnika
	Session 4 b: Power & Control Engineering	Papers: 32, 49, 90, 105, 126	DEC Classroom
3.45- 4.15 PM	HIGH TEA		
4.15 PM Onwards	Valedictory Function		Sambhram

Dr. Nagesh Prabhu
Program Chair

Dr. Rekha Bhandarkar
Program Chair